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1	INZZ	memory SAME output\$3 SAME tim\$3 SAME (mode OR standard OR type) SAME switch\$3 SAME latch\$3 SAME register	unrestricted	0	_
2	INZZ	memory AND output\$3 AND tim\$3 AND (mode OR standard OR type) AND switch\$3 AND latch\$3 AND register	unrestricted	0	-
3	INZZ	(select\$3 OR arbit\$3 OR arbitrat\$3) AND (buffer OR queue OR cache)	unrestricted	4734	show titles
4	INZZ	memory AND (mode OR standard OR type) AND time NEAR switch\$3	unrestricted	222	show titles
5	INZZ	3 AND 4	unrestricted	5	show titles
6	INZZ	flash OR ic OR integrated ADJ circuit NEAR card OR (non-volatile OR non ADJ volatile) WITH memory	unrestricted	86540	show titles
7	INZZ	4 AND 6	unrestricted	11	show titles

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- #1 ((memory and output and time and (mode or standard or type) and switch and latch and register)<in>metadata)
- #2 ((memory and output and time and (mode or standard or type) and switch and latch and register)<in>metadata)
- #3 ((memory and output and (mode or standard or type) and switch and latch)<IN>metadata)
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- #5 ((((memory and output and (mode or standard or type)) <in>metadata))<AND>(switch<in>metadata))
- ((((((memory and output and (mode or standard or type))

 **in>metadata))<and>(switch<in>metadata)))<AND>(timing and switch<in>metadata))
- #8 ((((((((memory and output and (mode or standard or type))
 <in>metadata))<and>(switch<in>metadata)))<and>(timing and
 switch<in>metadata)))<and>((hs-mmc or hsmmc or (high speed
 multimedia card) or (multimedia card))<in>metadata))
- #9 ((((((((memory and output and (mode or standard or type))
 <in>metadata))<and>(switch<in>metadata)))<and>(timing and
 switch<in>metadata)))<AND>((timing or clock and cycle)
 <in>metadata))
- ((((((((((memory and output and (mode or standard or type))
 <in>metadata))<and>(switch<in>metadata)))<and>(timing and
 switch<in>metadata)))<and>((timing or clock and cycle)
 <in>metadata)))<AND>(operation and (mode or standard)
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